

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-14. (Canceled)

15. (Currently amended) An active-matrix-type liquid crystal display panel comprising:

an active matrix substrate including on an insulating substrate: a plurality of pixel electrodes; pixel switching elements individually connected to the pixel electrodes; and a plurality of scanning lines and a plurality of data lines, provided in a lattice form, for driving the pixel electrodes through the pixel switching elements;

an opposing substrate including a common electrode, the opposing substrate being adhered to the active matrix substrate with a liquid crystal layer therebetween;

data-line inspection-use switching elements, individually connected to the plurality of data lines, for controlling a supply of an inspection-use display signal;

an inspection-use display signal line, provided for the data-line inspection-use switching elements, for supplying an inspection-use display signal to the data lines through the data-line inspection-use switching elements; and

a data-line inspection-use control signal line, provided for the data-line inspection-use switching elements, for inputting control signals for switching on/off the data-line inspection-use switching elements;

wherein a voltage for switching off the data-line inspection-use switching elements is applied to the data-line inspection-use control signal line while the liquid crystal display panel is driven so that all data-line inspection-use switching elements are off when the liquid crystal display panel is driven.

16. (Original) An active-matrix-type liquid crystal display panel comprising:

an active matrix substrate including on an insulating substrate: a plurality of pixel electrodes; pixel switching elements individually connected to the pixel electrodes; and a plurality of scanning lines and a plurality of data lines, provided in a lattice form, for driving the pixel electrodes through the pixel switching elements;

an opposing substrate including a common electrode, the opposing substrate being adhered to the active matrix substrate with a liquid crystal layer therebetween;

scanning-line inspection-use switching elements, individually connected to the plurality of scanning lines, for controlling a supply of an inspection-use scanning signal;

an inspection-use scanning signal line, provided for the scanning-line inspection-use switching elements, for supplying an inspection-use scanning signal to the scanning lines through the scanning-line inspection-use switching elements; and

a scanning-line inspection-use control signal line, provided for the scanning-line inspection-use switching elements, for inputting control signals for switching on/off the scanning-line inspection-use switching elements;

wherein a voltage for switching off the scanning-line inspection-use switching elements is applied to the scanning-line inspection-use control signal line while the liquid crystal display panel is driven.

17. (Original) The active-matrix-type liquid crystal display panel as set forth in claim 15, further comprising on the insulating substrate:

an external circuit for driving the liquid crystal display panel; and

wiring for driving the external circuit, the wiring including: first wiring for applying a ground potential; second wiring to which a voltage for switching off a switching element inside a logic of the external circuit is applied; and third wiring to which a voltage for defining a low level of an output voltage of the external circuit is applied;

wherein the data-line inspection-use control signal line is connected to any one of the first to third wiring.

18. (Original) The active-matrix-type liquid crystal display panel as set forth in claim 16, further comprising on the insulating substrate:

an external circuit for driving the liquid crystal display panel; and

wiring for driving the external circuit, the wiring including: first wiring for applying a ground potential; second wiring to which a voltage for switching off a switching element inside a logic of the external circuit is applied; and third wiring to which a voltage for defining a low level of an output voltage of the external circuit is applied;

wherein the scanning-line inspection-use control signal line is connected to any one of the first to third wiring.

19. (Currently amended) An active-matrix-type liquid crystal display panel comprising:

an active matrix substrate including on an insulating substrate: a plurality of pixel electrodes; pixel switching elements individually connected to the pixel electrodes; and a plurality of scanning lines and a plurality of data lines, provided in a lattice form, for driving the pixel electrodes through the pixel switching elements;

an opposing substrate including a common electrode, the opposing substrate being adhered to the active matrix substrate with a liquid crystal layer therebetween;

data-line inspection-use switching elements, individually connected to the plurality of data lines, for controlling a supply of an inspection-use display signal;

an inspection-use display signal line, provided for the data-line inspection-use switching elements, for supplying an inspection-use display signal to the data lines through the data-line inspection-use switching elements;

a data-line inspection-use control signal line, provided for the data-line inspection-use switching elements, for inputting control signals for switching on/off the data-line inspection-use switching elements;

wherein a voltage for switching off the data-line inspection-use switching elements is applied to the data-line inspection-use control signal line while the liquid crystal display panel is driven

further comprising on the insulating substrate: an external circuit for driving the liquid crystal display panel; and wiring for driving the external circuit, the wiring including first wiring for applying a ground potential, second wiring to which a voltage for switching off a switching element inside a logic of the external circuit is applied, and third wiring to which a voltage for defining a low level of an output voltage of the external circuit is applied;

wherein the data-line inspection-use control signal line is connected to any one of the first to third wiring; and

~~The active matrix type liquid crystal display panel as set forth in claim 17, further comprising~~ a resistive element between one of the first to third wiring, connected to the data-line inspection-use control signal line, and the data-line inspection-use switching elements to which the data-line inspection-use control signal line is connected.

20. (Original) The active-matrix-type liquid crystal display panel as set forth in claim 18, further comprising a resistive element between one of the first to third wiring,

connected to the scanning-line inspection-use control signal line, and the scanning-line inspection-use switching elements to which the scanning-line inspection-use control signal line is connected.

21. (Original) The active-matrix-type liquid crystal display panel as set forth in claim 19, wherein the resistive element is formed by a non-linear element manufactured in a same process as the pixel switching elements.

22. (Original) The active-matrix-type liquid crystal display panel as set forth in claim 20, wherein the resistive element is formed by a non-linear element manufactured in a same process as the pixel switching elements.

23. (Currently amended) The active-matrix-type liquid crystal display panel as set forth in claim ~~[[21]]~~ 19, wherein the resistive element is formed by a plurality of the non-linear elements connected to each other in series.

24. (Currently amended) The active-matrix-type liquid crystal display panel as set forth in claim ~~[[22]]~~ 20, wherein the resistive element is formed by a plurality of the non-linear elements connected to each other in series.

25-87. (Canceled)